

PRODUCT SHEET

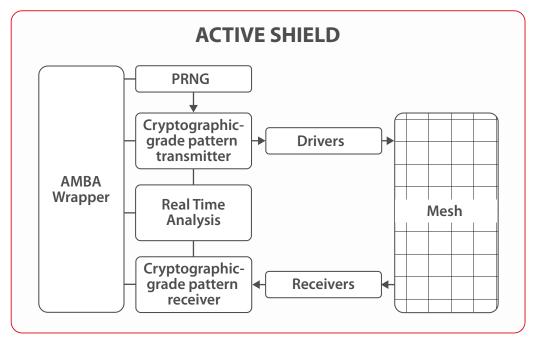
Securyzr<sup>™</sup> > Securyzr<sup>™</sup> Secure HW Solutions > Securyzr<sup>™</sup> Security IP > Anti-Tamper > Active Shield > SCZ\_IP\_AS\_200

# **ACTIVE SHIELD**

Attacks on digital circuits can occur when an attacker attempts to physically alter the device's internal components. These types of attacks are known as intrusive and can include actions such as probing or manipulating signals, adding or removing components, or modifying features on the chip.

To counteract these threats, Secure-IC has developed the Active Shield technology. This technology uses a protective mesh layer over the sensitive parts of the circuit, and actively monitors the mesh integrity for any signs of tampering. This countermeasure helps to safeguard the circuit's features and components, such as metal routing and transistors, from unauthorized access or modification through the front side of the device, such as:

- Wire micro-probing to read or force an equipotential
- Wire cutting (e.g., alarms, entropy source disconnection from a true random number generator...)
- Wire re-routing
- Burnt fuses opening
- ROM Altering

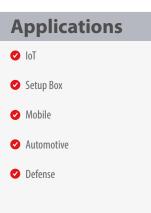


#### **Features**

- Anti-tamper solution with a mesh placed over the sensitive parts of the circuit and actively monitored
- Uses random cryptographically generated patterns to detect integrity violations
- Antenna-effect safe
- Security certification ready (incl. Common Criteria)

- Fully digital and designed with the standard cells library
- Mesh designed with scripts
- Mesh can be interleaved with P/G network in topmost layers (no need to sacrifice a layer)
- Compatible with clock gating to reduce the power consumption

- Transferable to any design kit
- Lightweight
- No calibration after design
- Easy to integrate into the system
- AMBA (APB, AHB, AXI) interface



# **Active monitoring**

In order to further deter intrusive attacks, the mesh is actively monitored using random cryptographically generated patterns to detect integrity violations. By using this technology, modifying and rerouting the mesh becomes very costly as the attacker must reroute many wires to avoid detection. In addition, the data travelling through the shield mesh cannot be predicted by the attacker, because it is output by a cryptographic block cipher.

Active Shield technology relies on several submodules:

- A mesh created using a metal layer for wire routing
- Drivers and receivers used for electrical connection with the mesh
- A cryptographic-grade tamper detection module for monitoring the mesh integrity

### **Ideal for**

Active Shield IP is ideal for:

- Protection against Bus probing
- Protection against FIB attacks
- Protection against wire micro-probing to read or force an equipotential
- Protection against wire cutting (e.g., alarms, entropy source disconnection from a true random number generator)
- Protection against wire re-routing
- Protection against ROM altering

# **Deliverables**

- Technical specifications
- ✓ RTL of the AMBA wrapper
- ✓ Constraints file (SDC)
- Remote support for integration
- 🕑 User guide
- Post-synthesis generic netlist
- Innovus Back-end scripts (TCL) to place and route the mesh
- Test report documentation
- Self-checking RTL Testbench based on reference scenario for simulation. (Simulation scripts are adapted to Questasim, any change of Simulator shall be taken care of by the Licensee)

V1.2



#### HEADQUARTERS

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